

31046 U.S. PTO

10/032734

12/28/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10032734	12/28/2001	438 ²⁵⁷	725	2842	

**APPLICANTS: Akram Salman; Hembree David; Wark James;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/102,224 06/22/1998
WHICH IS A CON OF 08/614,301 03/12/1996 PAT 5,807,762

See 032235

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no

ATTORNEY DOCKET NO

Verified and Acknowledged Examiners's initials

2754.4US (95-0742.4)

TITLE : Multi-chip module system and method of fabrication

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs.Drwg. Print Fig.
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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